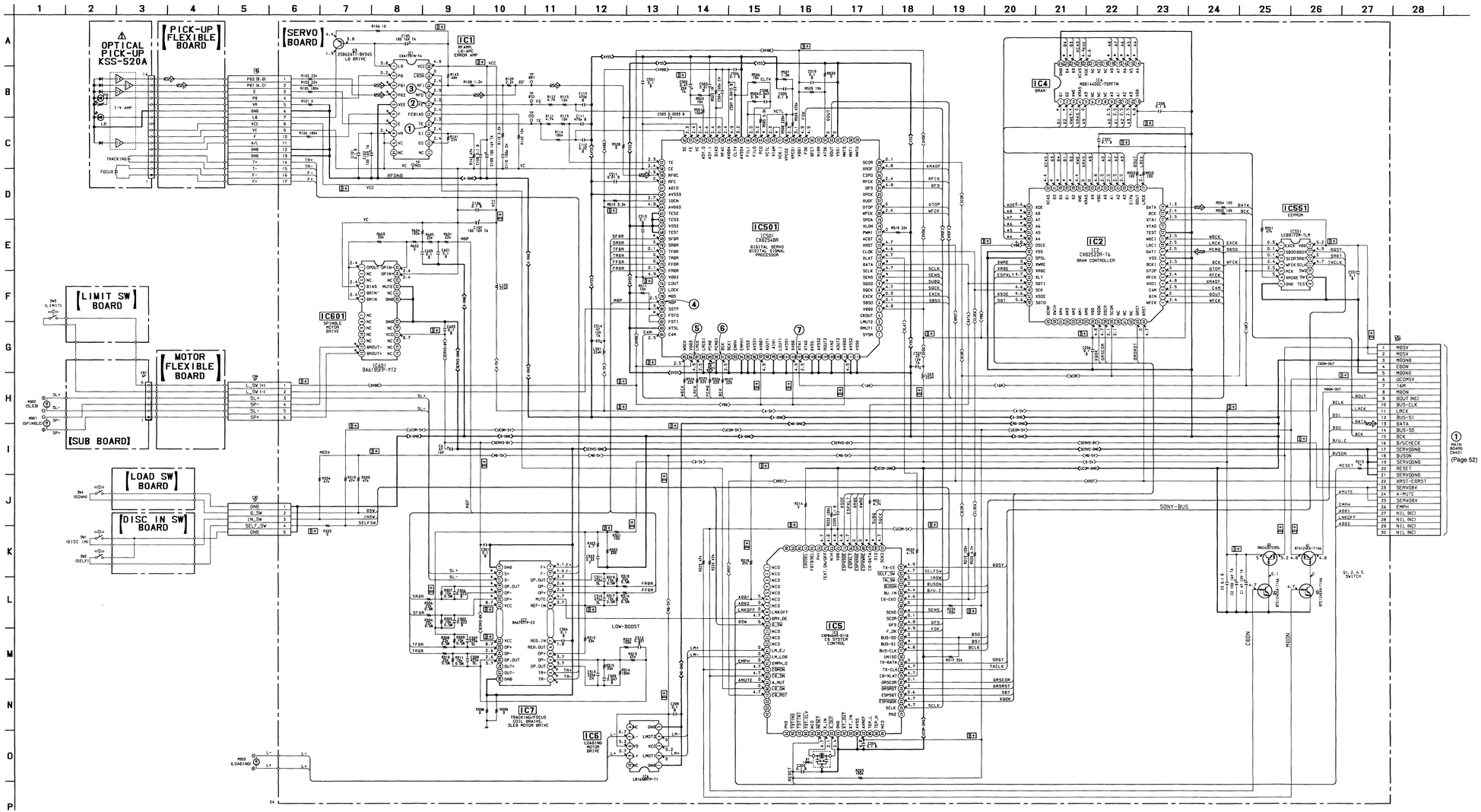
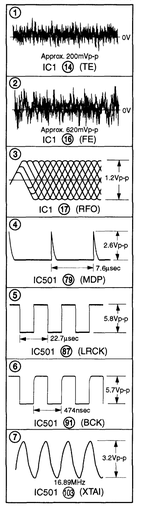
4-7. SCHEMATIC DIAGRAM — CD MECHANISM SECTION —

• Refer to page 67 for IC Block Diagrams.

SEE ADDITIONAL INFORMATION



•Waveforms (MODE:PLAY)


① Approx. 200mVp-p 0V IC1 ⑭ (TE)
② Approx. 620mVp-p 0V IC1 ⑯ (FE)
③ 1.2Vp-p IC1 ⑰ (RFO)
④ 2.6Vp-p 7.8μsec IC501 ⑱ (MDP)
⑤ 5.8Vp-p 22.7μsec IC501 ⑰ (LRCK)
⑥ 5.7Vp-p 474nsec IC501 ⑲ (BCK)
⑦ 3.2Vp-p 16.89MHz IC501 ㉘ (XTAI)

Note:

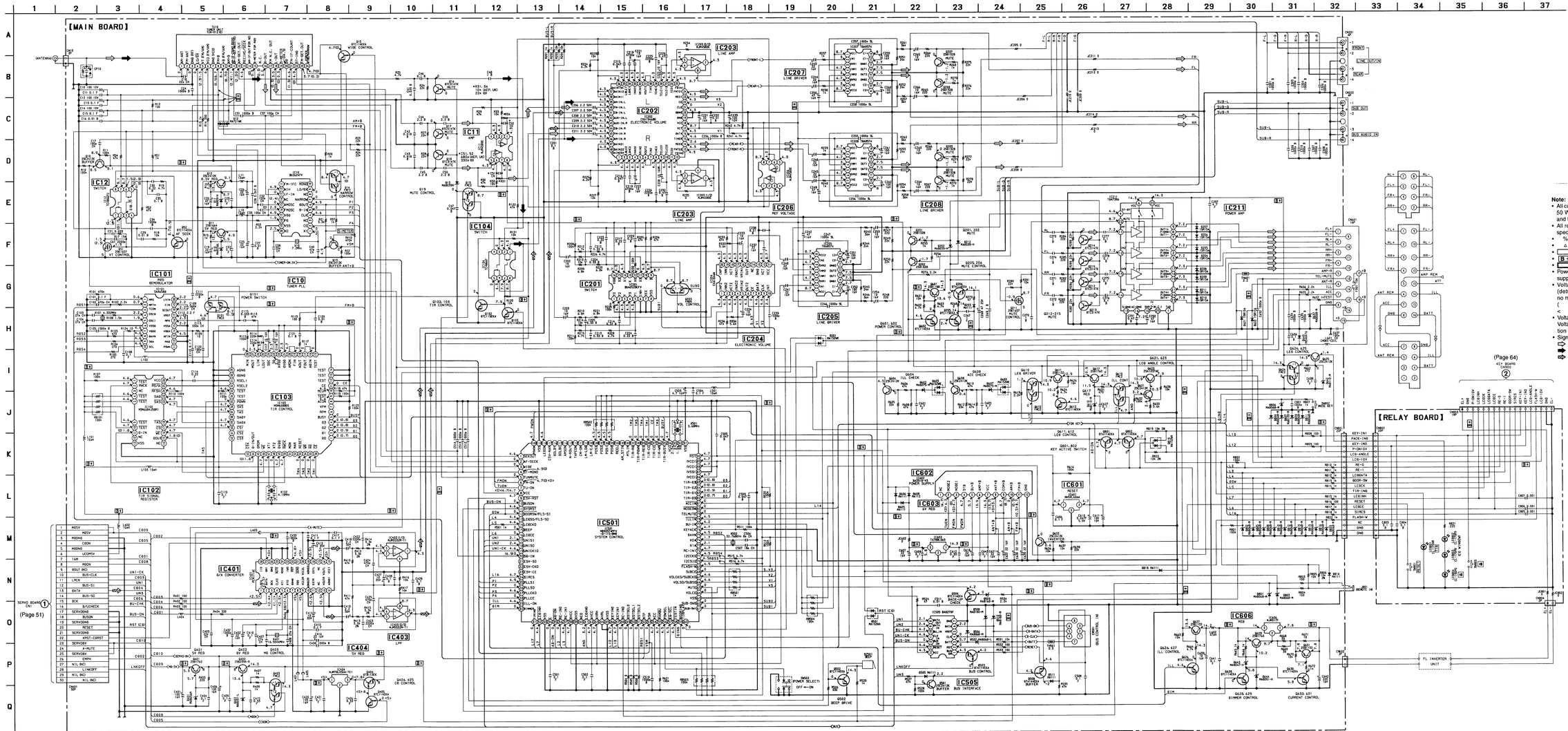
- All capacitors are in μF unless otherwise noted. pF: μμF 50 WV or less are not indicated except for electrolytics and tantaliums.
- All resistors are in Ω and 1/4 W or less unless otherwise specified.
- % : indicates tolerance.
- Δ : internal component.

Note: The components identified by mark Δ or dotted line with mark Δ are critical for safety. Replace only with part number specified.

- B+ : B+ Line.
- Power voltage is dc 14.4V and fed with regulated dc power supply from ACC and BATT cords.
- Voltage and waveforms are dc with respect to ground under no-signal conditions. no mark : CD PLAY
  * : Impossible to measure
- Voltages are taken with a VOM (Input impedance 10 MΩ). Voltage variations may be noted due to normal production tolerances.
- Waveforms are taken with a oscilloscope. Voltage variations may be noted due to normal production tolerances.
- Signal path. ⇨ : CD

SEE ADDITIONAL INFORMATION

4-8. SCHEMATIC DIAGRAM — MAIN SECTION — • Refer to page 69 for IC Block Diagrams.

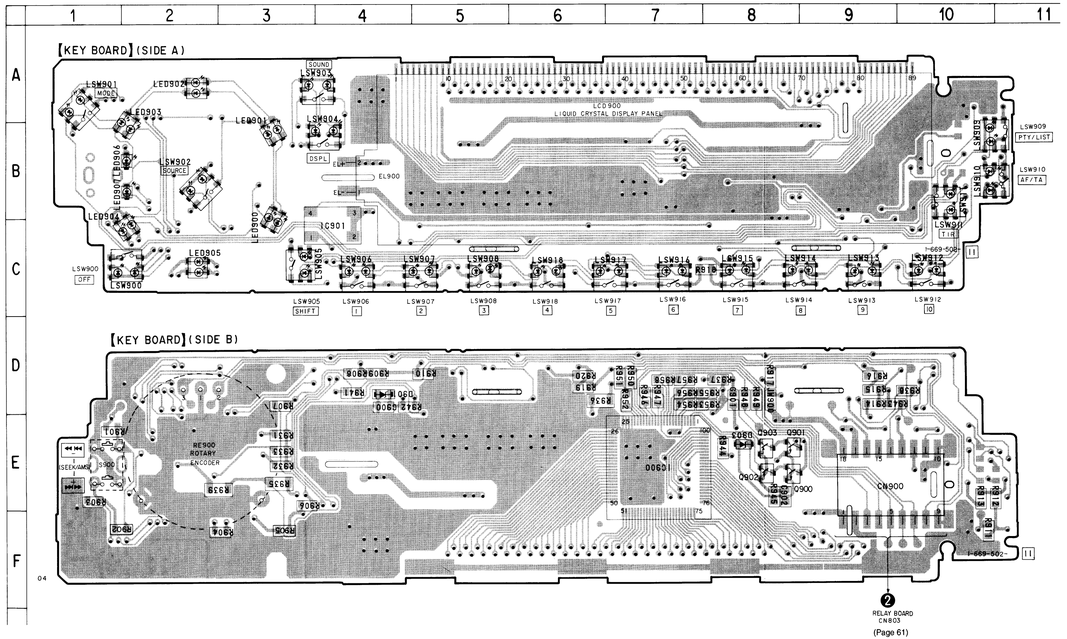


Note:
- All capacitors are in µF unless otherwise noted. pF: µµF 50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and ¼ W or less unless otherwise specified.
- % : indicates tolerance.
- Δ : internal component.
- ▭ : panel designation.
- B+ : B+ Line.
- ▭ : adjustment for repair.
- Power voltage is dc 14.4V and fed with regulated dc power supply from ACC and BATT cords.
- Voltage is dc with respect to ground under no-signal (detuned) condition.
  no mark : FM
  ( ) : MW
  < > : CD PLAY
- Voltages are taken with a VOM (Input impedance 10 MΩ). Voltage variations may be noted due to normal production tolerances.
- Signal path.
  ⇨ : FM
  ➡ : MW
  ⇨ : CD

4-10. PRINTED WIRING BOARD — DISPLAY SECTION —

• Semiconductor Location

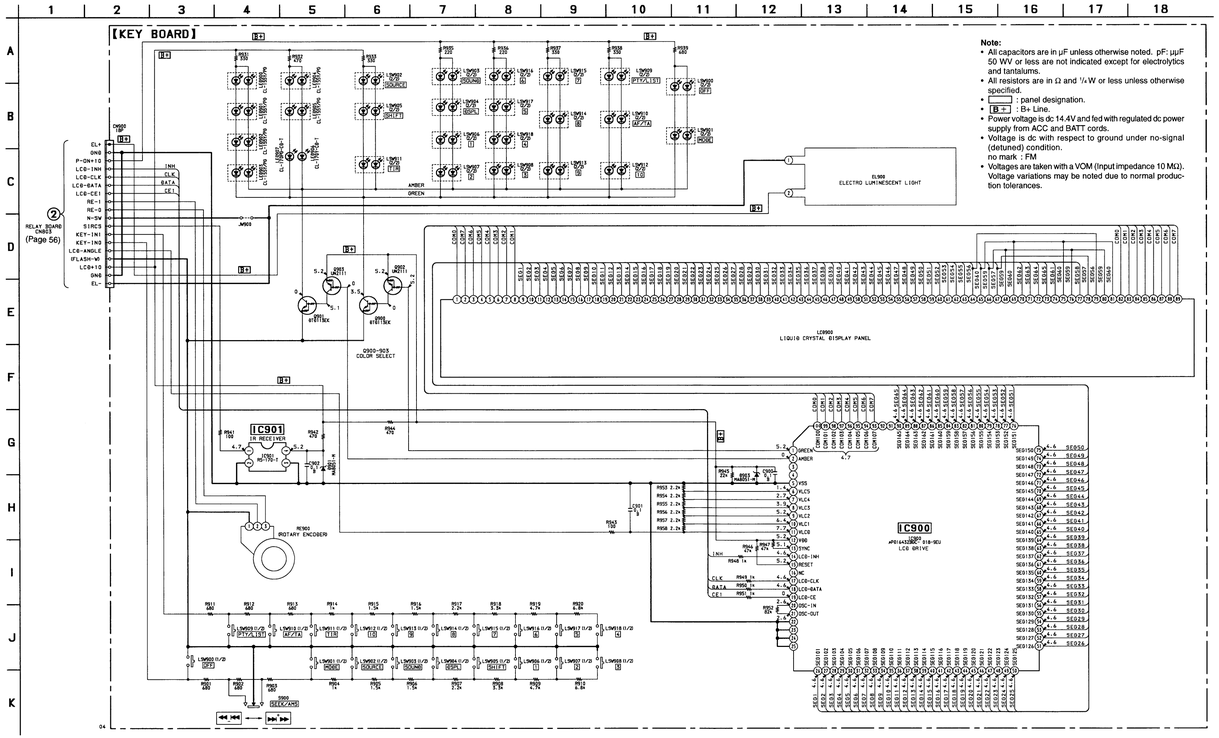
Ref. No.	Location
D901	D-4
D903	E-8
IC900	E-7
IC901	C-4
LED900	B-3
LED901	B-3
LED902	A-2
LED903	A-2
LED904	C-2
LED905	C-2
LED906	B-2
LED907	B-2
Q900	E-8
Q901	E-8
Q902	E-8
Q903	E-8



Note:
- ● : Through hole.
- : Pattern from the side which enables seeing. (The other layers' patterns are not indicated.)

4-11. SCHEMATIC DIAGRAM — DISPLAY SECTION —

SEE ADDITIONAL INFORMATION



Note:
- All capacitors are in µF unless otherwise noted. pF: µµF 50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and 1/4 W or less unless otherwise specified.
- : panel designation.
- B+ : B+ Line.
- Power voltage is dc 14.4V and fed with regulated dc power supply from ACC and BATT cords.
- Voltage is dc with respect to ground under no-signal (detuned) condition. no mark : FM
- Voltages are taken with a VOM (Input impedance 10 MΩ). Voltage variations may be noted due to normal production tolerances.